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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kuzmenka, et al. Docket No.: INF-129
Serial No: 10/759,103 Art Unit: 2818
Date Filed: January 20, 2004
Title: Buffer Amplifier Architecture for Semiconductor Memory Circuits

CERTIFICATION OF FACSIMILE TRANSMISSION

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- Associate Power of Attorney (1 page)
- Change of Correspondence Address (1 page)
- Information Disclosure Statement (1 page)
- IDS Form PTO/SB/08a (1 page) citing (2) references

Respectfully submitted,

Natalie Swider

Natalie Swider
Legal Assistant

Confirmation Respectfully Requested

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For: Buffer Amplifier Architecture for Semiconductor Memory Circuits

Mail Stop: Amendment
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Applicants wish to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO/SB/08a that may be considered material to the examination of the above-identified application.

No fee is due at this time, as this Information Disclosure Statement is being filed pursuant to 37 C.F.R. § 1.97(b)(3), before the mailing of a first Office action on the merits.

Respectfully submitted,



Ira S. Matsil
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10/5/2004

Date

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